

G₃

F₄

P₃

G₃

A₁

U₁



HPC: para la creación de medicamentos, explotación de GPUs y FPGAs

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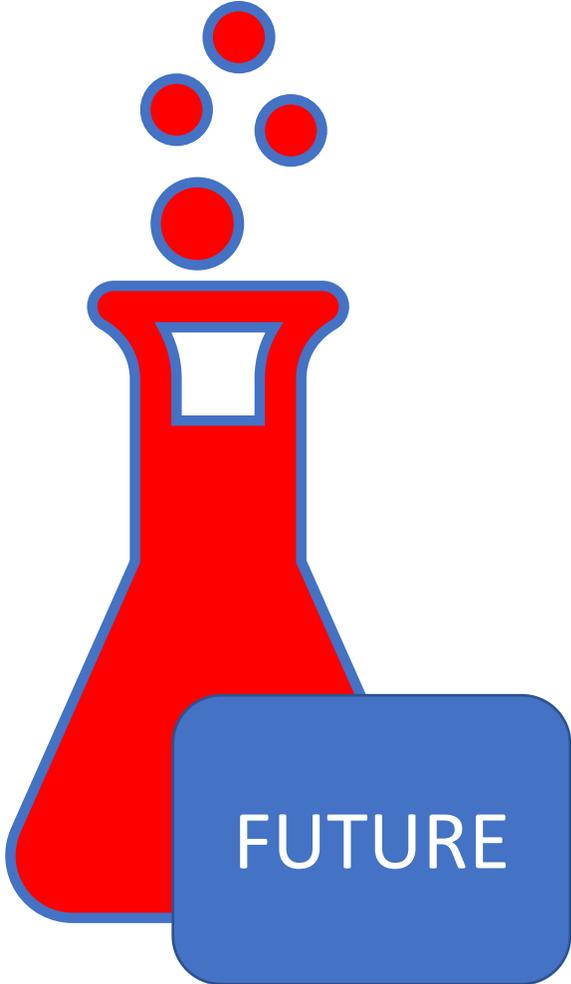


The
experiment

PAST

What we
do?

PRESENT



FUTURE

ENLACE PARA VER EL VIDEO:

**[http://www.sie.es/wp-content/
uploads/2021/05/
multimedia1_KcXbZjuf.mp4](http://www.sie.es/wp-content/uploads/2021/05/multimedia1_KcXbZjuf.mp4)**

What we do? Determining atomic structures

3GWH
Workgroup

<https://www.nature.com/articles/nmeth.1365>

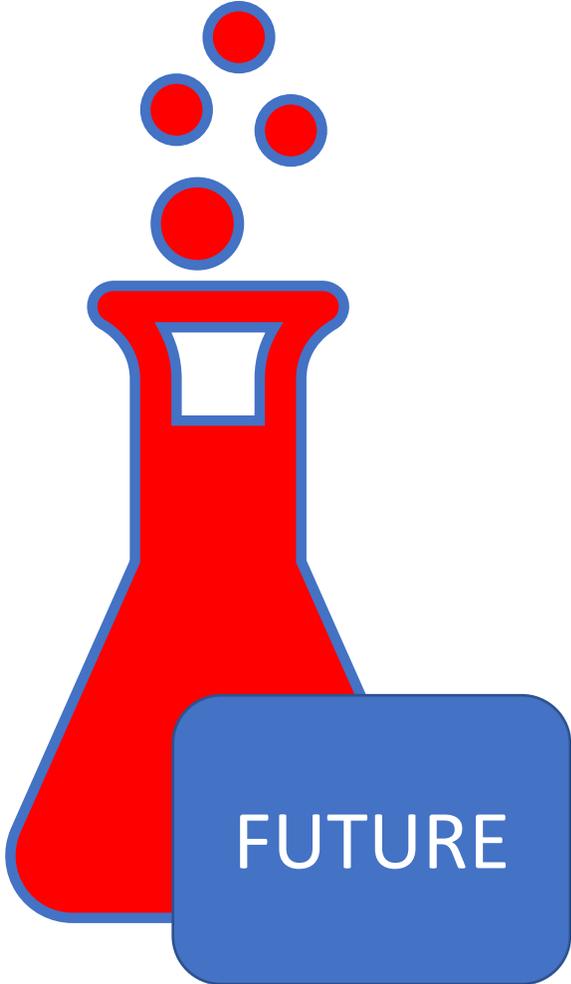


The
experiment

PAST

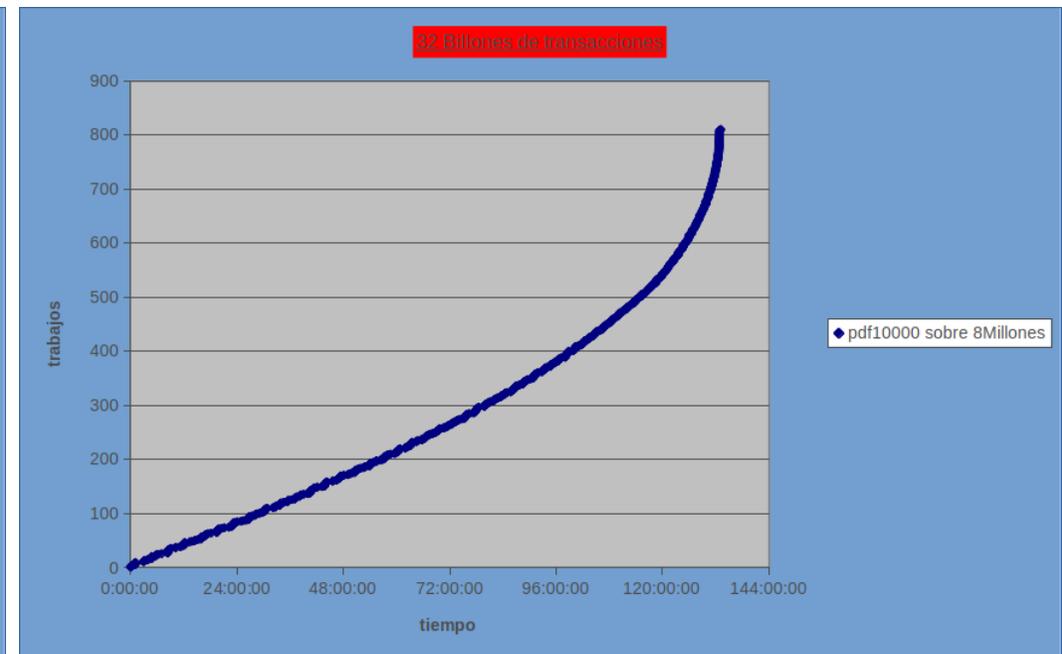
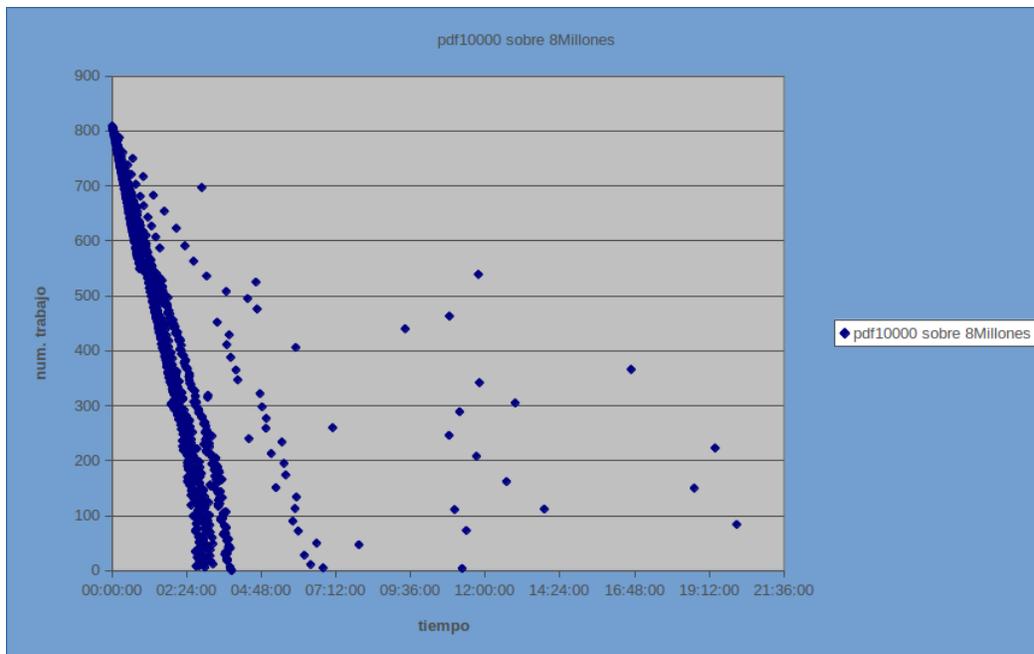
What we
do?

PRESENT



FUTURE

Where we came from? Year 2001, grid



Year 2011, GPUs

```
__global__ void VecAdd(int* A, float*AP, const float* B1, const float* B2,
const float* B3, const float* B4, const int N, const int ii)
{
    int i = blockDim.x * blockIdx.x + threadIdx.x;
    if ((ii < i) && (i < N))
        A[i] = 0; AP[i] =0;
        A[i] = int(sqrt((B1[ii]-B1[i])*(B1[ii]-B1[i]) +
                        (B2[ii]-B2[i])*(B2[ii]-B2[i]) +
                        (B3[ii]-B3[i])*(B3[ii]-B3[i]))));
        AP[i] = B4[ii]*B4[i];
}
```

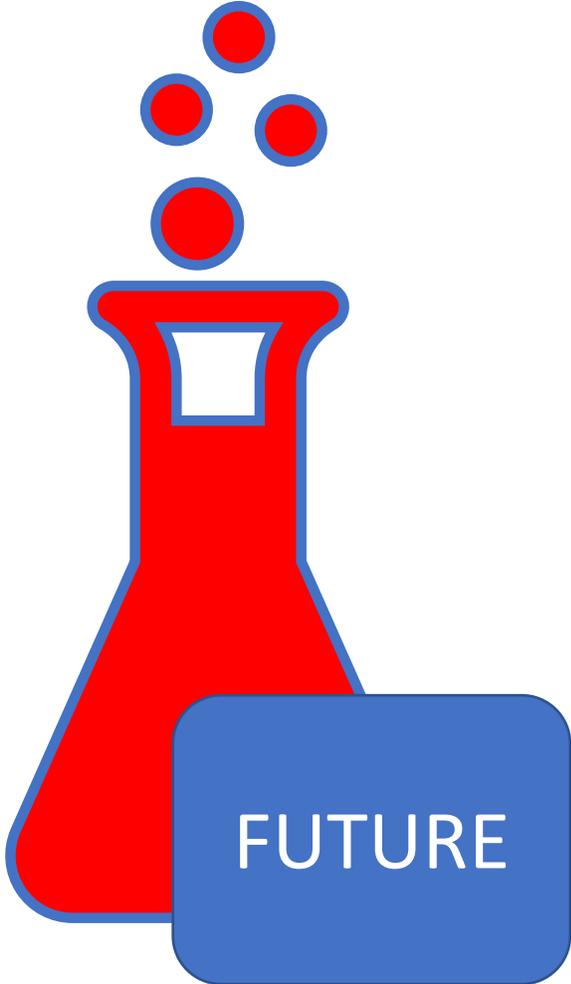


The experiment

PAST

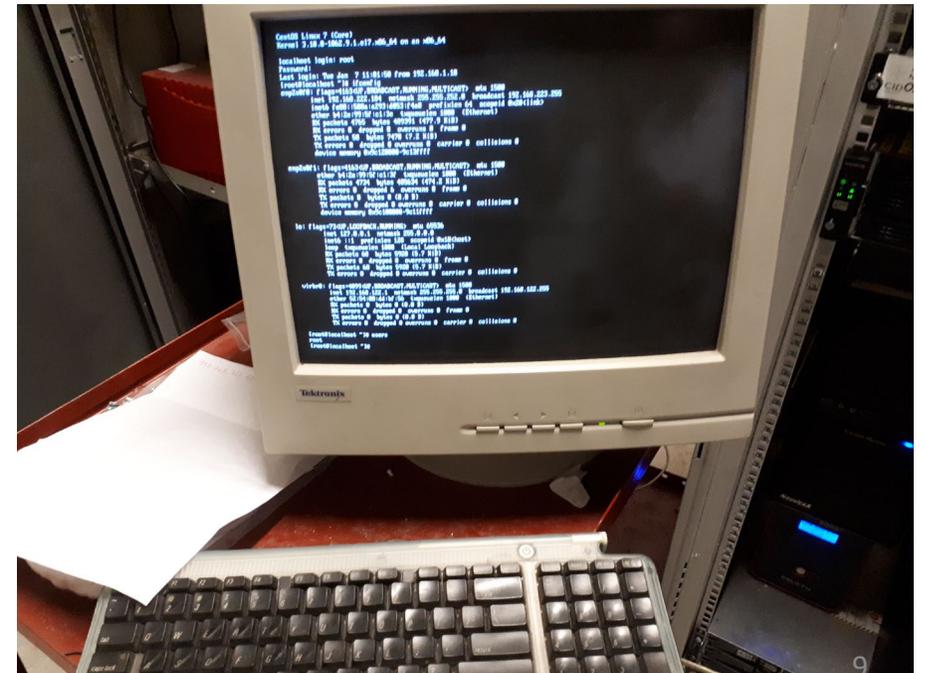
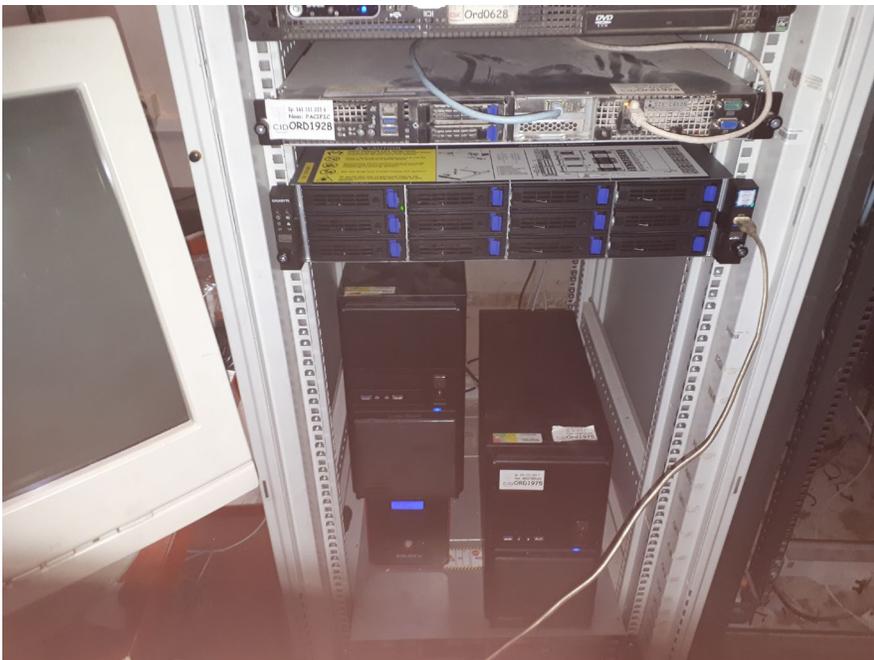
What we do?

PRESENT



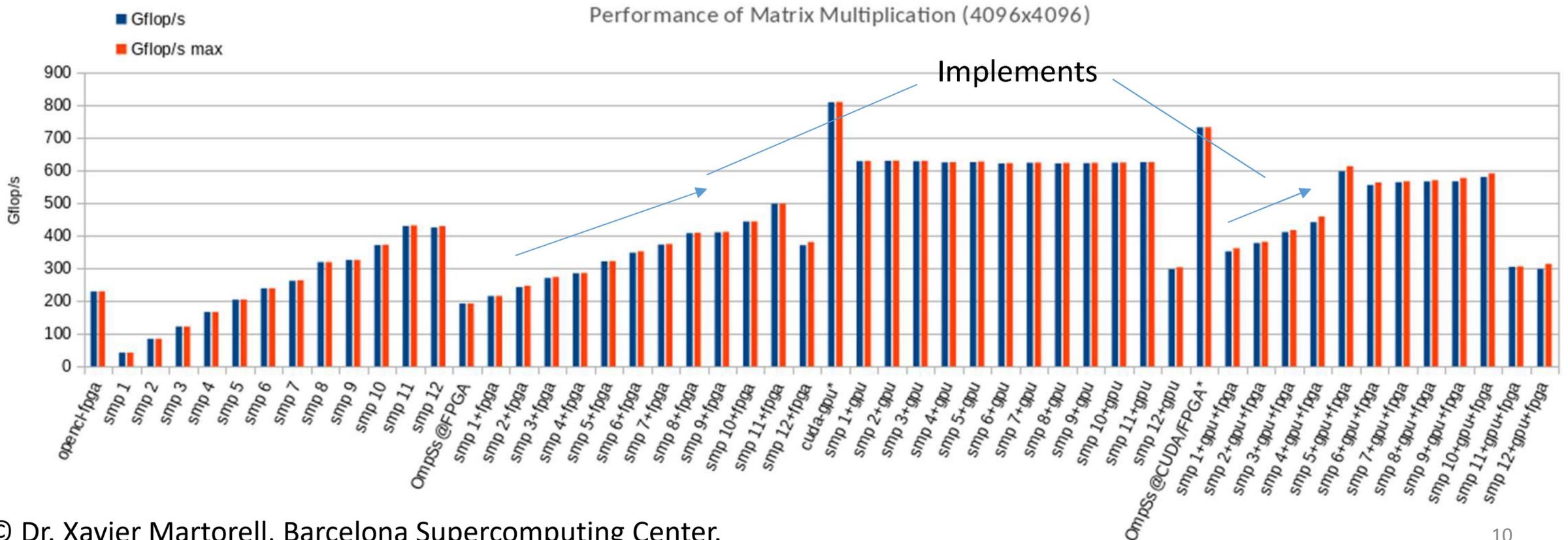
The equipment

- 1 Intel FPGA STRATIX 10 Model GX2800
- 1 GPU NVidia RTX 2070 SUPER™ Blower, 8 GB RAM GDDR6 2.560 cores
- 2 Processors Xeon Cascade-lake Bronze 3204 de 1,9 GHz, six cores each



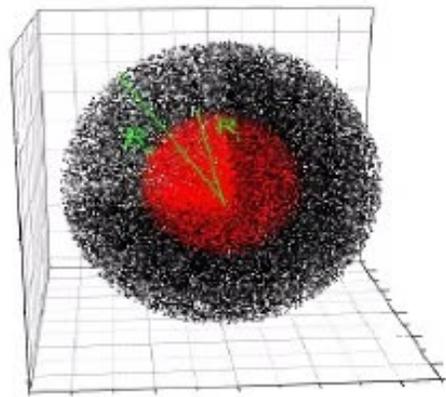
Runtimes– OmpSs@CUDA & OpenCL

- Evaluation on Intel CPUs, Nvidia GPU and Intel Stratix 10 FPGA
 - Lesson learned: devices similar in performance contribute better to overall performance
- 6-core Intel Xeon® Bronze 3204 @1.9GHz, no ht
 Nvidia GeForce RTX 2070 SUPER
 Intel Stratix 10 FPGA

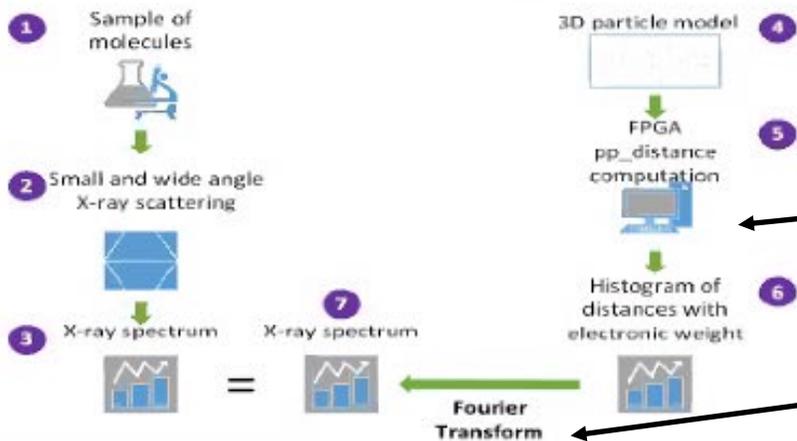
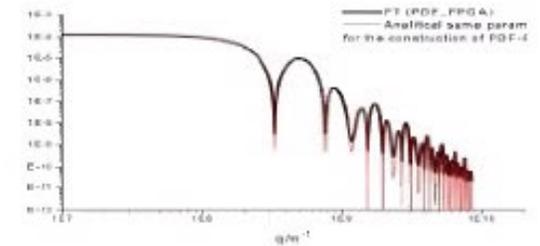
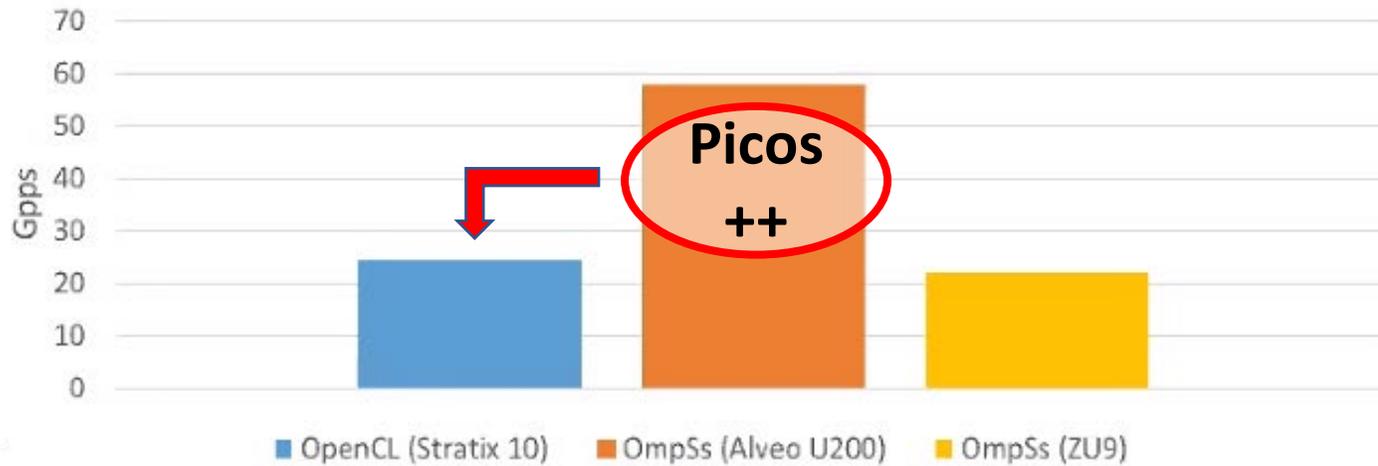


Spectra results (OpenCL comparison)

+



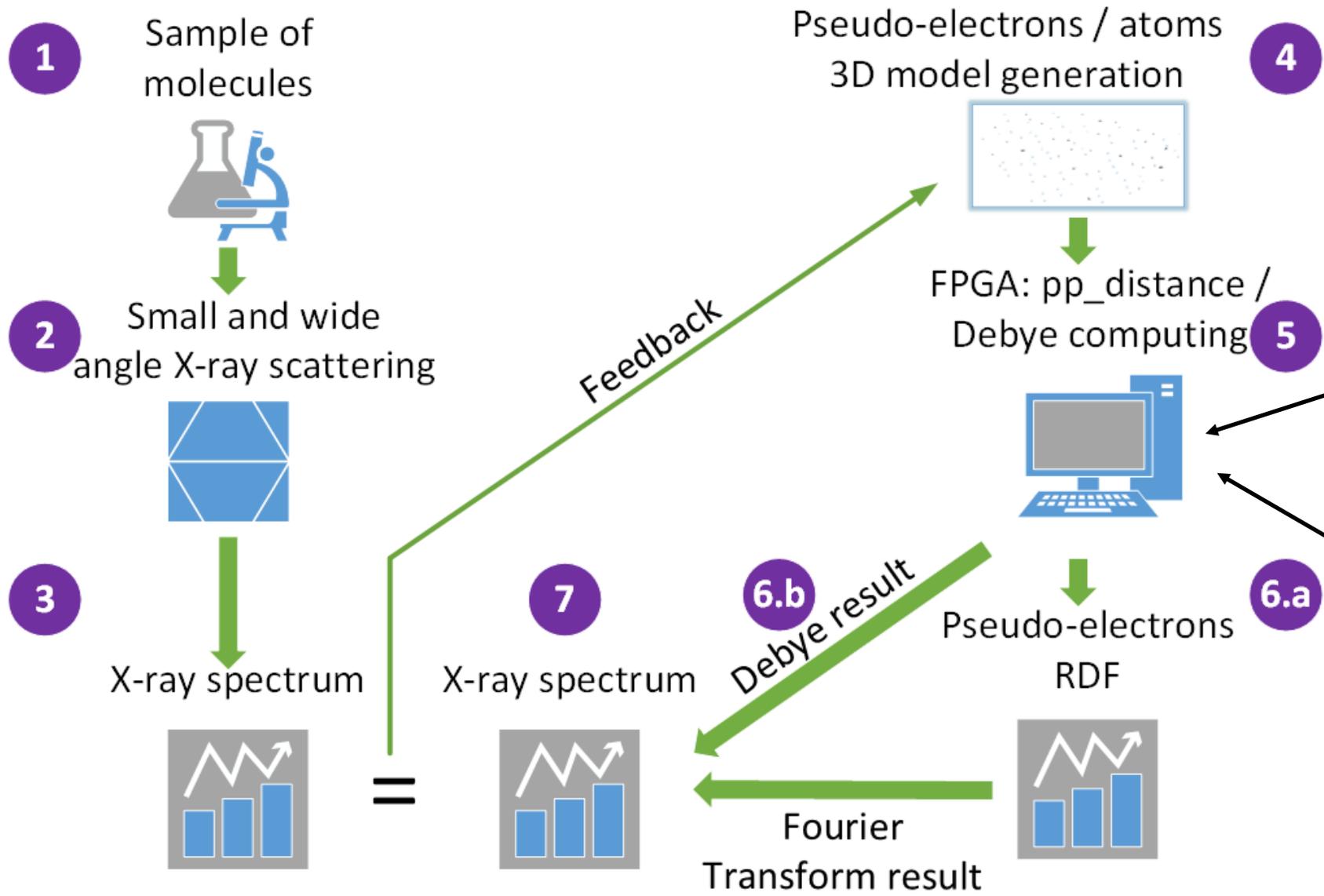
Spectra Performance



$$\forall i \forall j, \text{ histogram}[d_{ij}] += e_i \cdot e_j$$

$$P(q) = 4\pi \int_0^\infty (p) r \frac{\sin(qr)}{qr} dr$$

Fourier transform in the sine



Atomic Form Factors

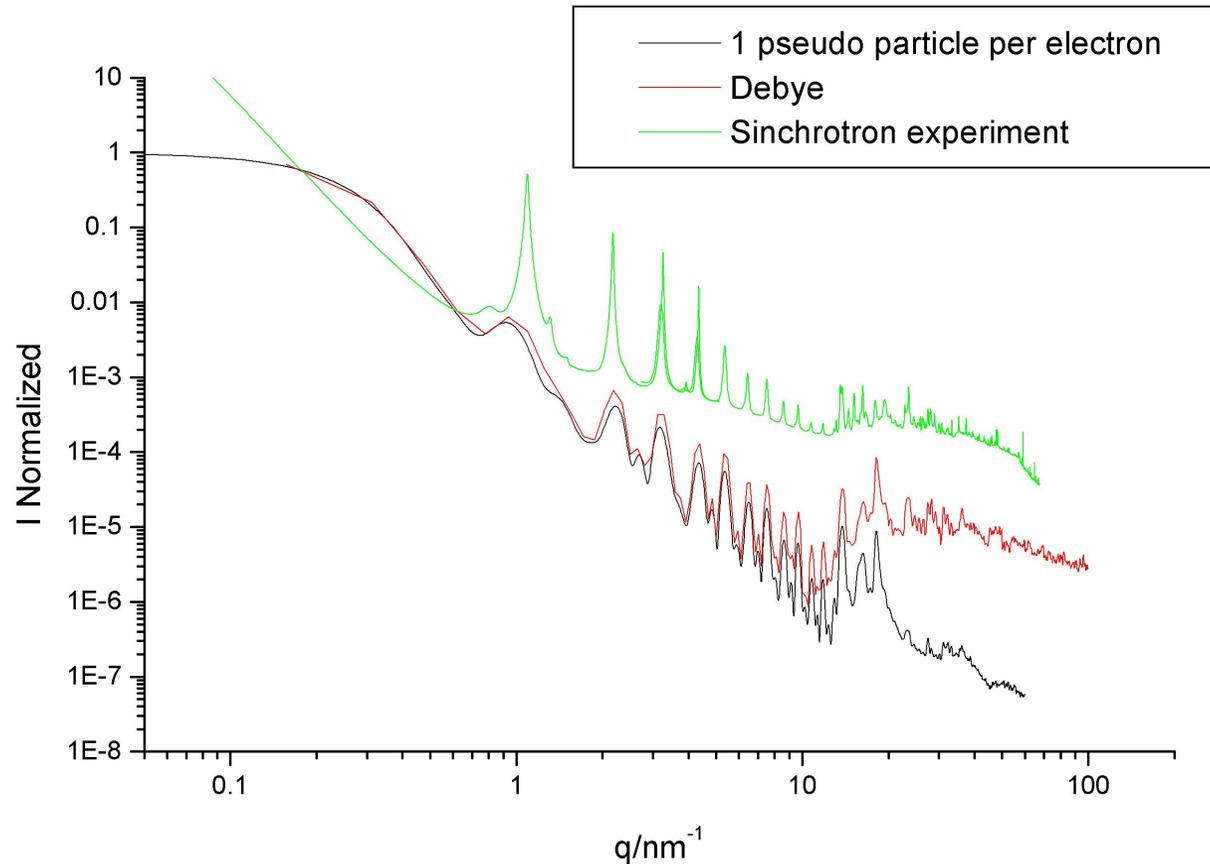
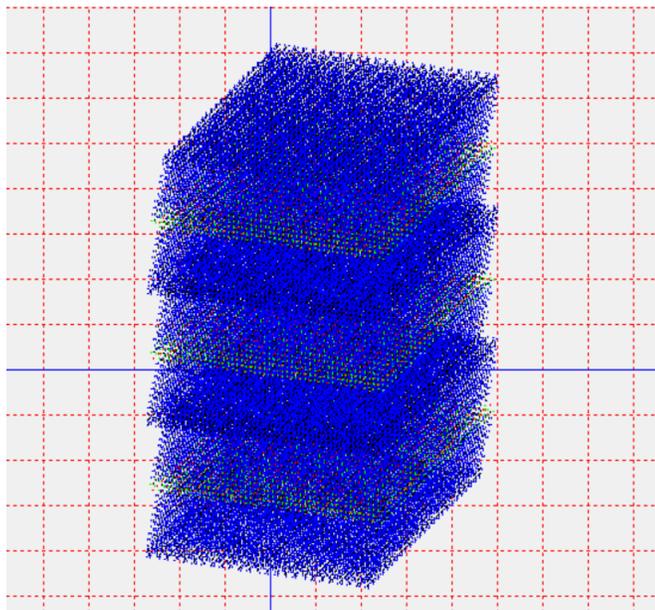
$$f(q) = \left(\sum_{i=1}^4 a_i e^{-b_i \left(\frac{q}{4\pi}\right)^2} \right) + c$$

$$I(q) = \sum_{i=1}^N \sum_{j=1}^N f_i f_j \frac{\sin(q \cdot r_{ij})}{q \cdot r_{ij}}$$

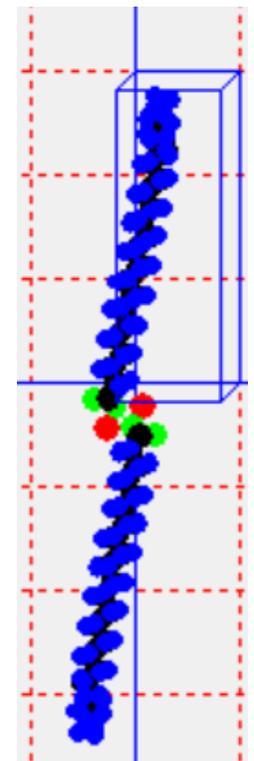
Debye formula

Comparison pseudo-electrons/atoms/experimental

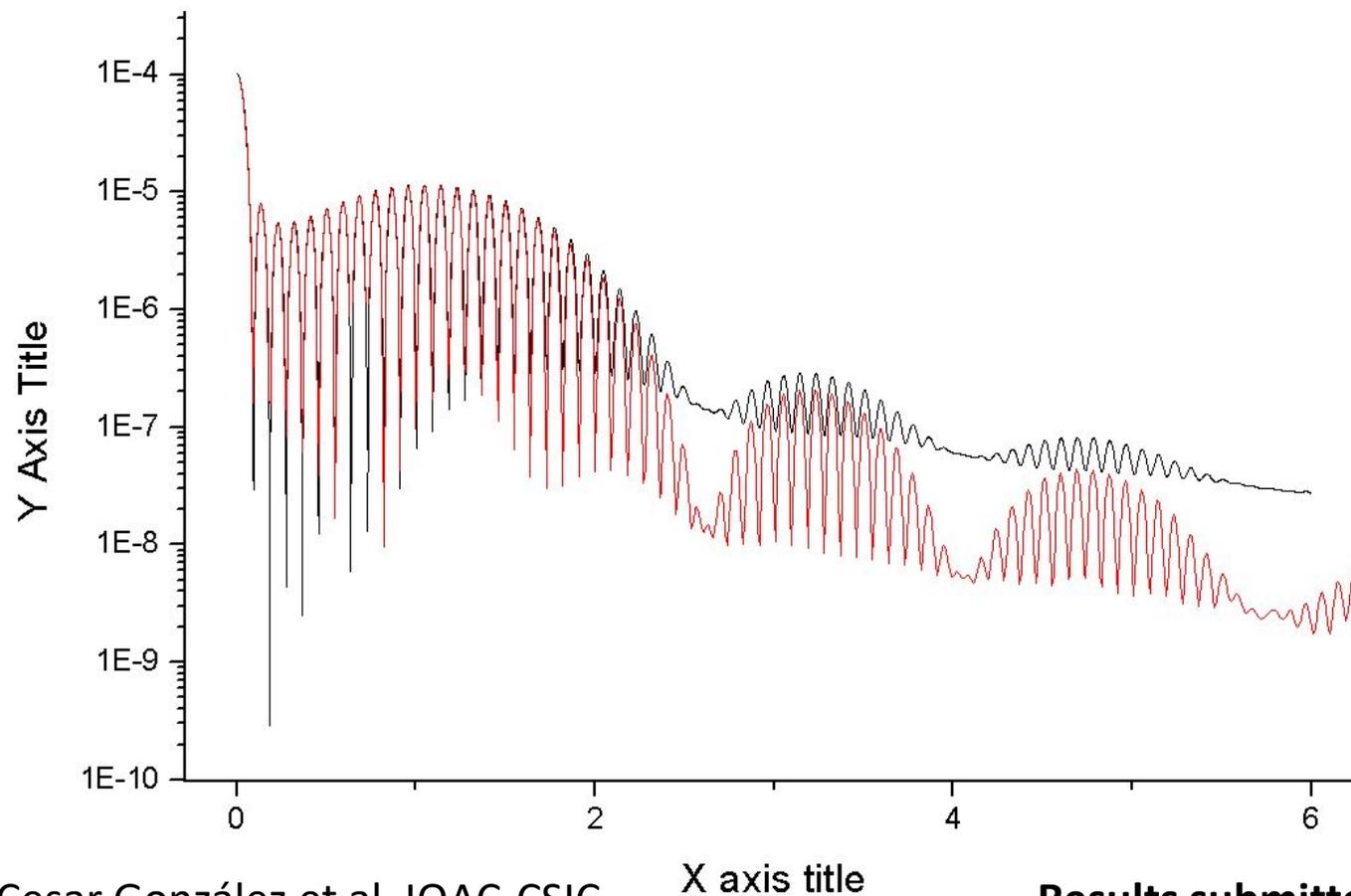
3D model



$\text{AgC}_{22}\text{H}_{43}\text{O}_2$



Comparison and more ...



Explaining the results



Latency, 80 sec.

Picos ++, 34 sec.

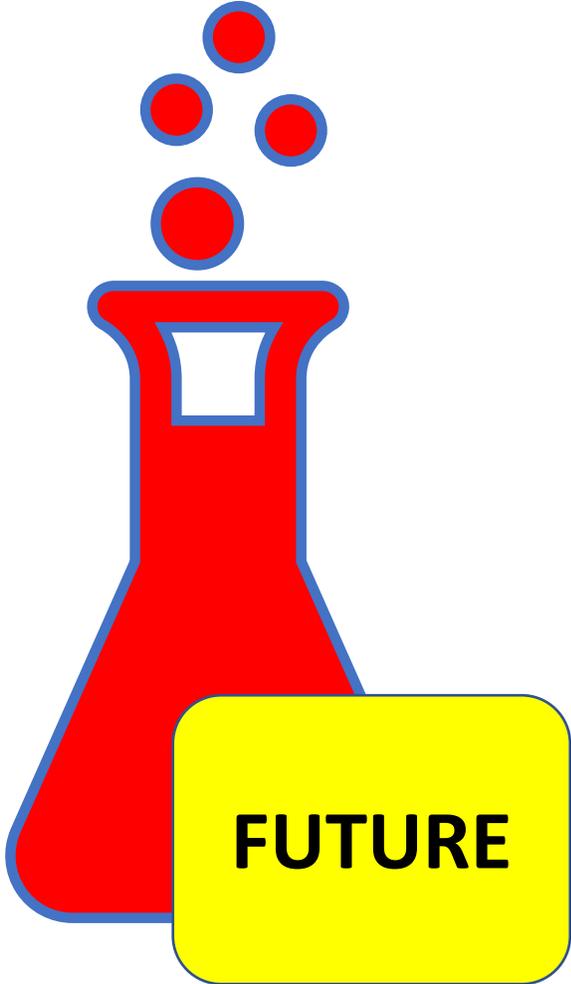


The experiment

PAST

What we do?

PRESENT





Road map to the structures

- 1. Model the real structure of ??? with atoms and try to converge a random model to its spectrum with pseudo-electons.**
- 2. Try to converge a random model to the BSA real spectrum.**
- 3. Try to converge a random model to an unknown structure.**

THE END: Performance summary

YEAR	Technologies	Time	Place
Before 2001	CPU	Week	IQAC-CSIC lab
2001	Grid	Weekend	CID-CSIC
2011	GPU	10,000 seconds	CID-CSIC lab
2021	FPGA	30 seconds	BSC

*Breaking
news !*

C-CUDA	Bpps	seg.
K40	14	142,86
GTX 1060	50	40,00
GTX 1670	116	17,24
GTX 1080	188	10,64
<i>RTX 2070</i>	218	9,17

GPU (*pseudo-electrons*)

RDF F vector affect performance: x10 → time x2



Dr. Ramon Pons
FORTRAN

César González /
Dr. Ramon Pons

César González
C-CUDA
PC

7325
sec.

La industria ha estado tomando un camino tecnológico en el que el escalado tradicional está llegando a su fin y el rendimiento de las aplicaciones y la eficiencia energética varían más de 2 órdenes de magnitud dependiendo de su paralelismo, heterogeneidad y localidad.

Dr. Wen – Mei Hwu, Senior Distinguished Research Scientist en NVIDIA ...

11 de mayo de 2021

César González /
Dr. Xavier Martorell
OpenCL@OmpSs
Server-FPGA

3708
sec.

116
sec.

Maurizio Paolini
C-OpenCL
Server-FPGA

César González
(migration)
OpenCL@OmpSS
Server-FPGA

80
sec.

34
sec.

César González
(migration)
C-CUDA
Server-CUDA

9
sec.

Moore's law



**“You can dream, but you can never go back
the way you came.”**

Jackson Browne, 9-JUN-1977